



# Vivekanand Education Society's Institute of Technology

Since 1962

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

## 3.3.1 Number of Ph.Ds registered per eligible teacher during the last five years

### 3.3.1.1. How many Ph.Ds registered per eligible teacher within last five years

HEI Input : 11

### 3.3.1.2. Number of teachers recognized as guides during the last five years

#### DVV QUERY

Provide Ph.d Certificate of faculties along with the name of the research scholars / Guide during the assessment period.

	Name of The PhD Scholar	Name of the Department	Name of the Guide
1	<a href="#">Mrs. Nilima Warke</a>	Electronics Engineering	<a href="#">Dr. (Smt.) Jayalekshmi M. Nair (Th./ICD/2020-21/432 dt. 10.11.2020)</a> <a href="#">Dr.P.P.Vaidya</a>
2	<a href="#">Mrs. Jayamala Adsul</a>	Electronics Engineering	<a href="#">Dr. (Smt.) Jayalekshmi M. Nair (Th./ICD/2020-21/432 dt. 10.11.2020)</a> <a href="#">Dr.P.P.Vaidya</a>
3	<a href="#">Mrs. Asma Siddavatam</a>	Electronics Engineering	<a href="#">Dr. (Smt.) Jayalekshmi M. Nair (Th./ICD/2020-21/432 dt. 10.11.2020)</a>
4	<a href="#">Mrs. Shilpa Joshi</a>	Electronics Engineering	<a href="#">Dr. Ramesh K. Kulkarni (Th./ICD/2020-21/432 Dt. 10.11.2020)</a>
5	<a href="#">Mrs. Kadambari Sharma</a>	Electronics Engineering	<a href="#">Dr. (Smt.) Jayalekshmi M. Nair (Th./ICD/2020-21/432 dt. 10.11.2020)</a> <a href="#">Dr.P.P.Vaidya</a>
6	<a href="#">Mrs. Lekshmi Ajesh</a>	Electronics Engineering	<a href="#">Dr. Ramesh K. Kulkarni (Th./ICD/2020-21/432 Dt. 10.11.2020)</a> <a href="#">Dr.P.P.Vaidya</a>
7	<a href="#">Mrs. Kavita Tewari</a>	Electronics Engineering	<a href="#">Dr. Ramesh K. Kulkarni (Th./ICD/2020-21/432 Dt. 10.11.2020)</a>
8	<a href="#">Mrs. Naveeta Kant</a>	Electronics Engineering	<a href="#">Dr. Ramesh K. Kulkarni (Th./ICD/2020-21/432 Dt. 10.11.2020)</a>
9	<a href="#">Mrs. Sangeetha Prasanna Ram</a>	Electronics Engineering	<a href="#">Dr. (Smt.) Jayalekshmi M. Nair (Th./ICD/2020-21/432 dt. 10.11.2020)</a>
10	<a href="#">Mrs. Kanchan Chavan</a>	Electronics Engineering	<a href="#">Dr. (Smt.) Jayalekshmi M. Nair (Th./ICD/2020-21/432 dt. 10.11.2020)</a> <a href="#">Dr.P.P.Vaidya</a>
11	<a href="#">Ms. Tejashree Phatak</a>	Electronics Engineering	<a href="#">Dr. (Smt.) Jayalekshmi M. Nair (Th./ICD/2020-21/432 dt. 10.11.2020)</a>



# Indian Institute of Technology, Bombay

Upon recommendation of the Senate hereby confers the degree of

## Doctor of Philosophy

on Jayalekshmi M. Nair

who has fulfilled the requirements for the degree as prescribed under the regulations and successfully defended in July 1995, the thesis entitled :

"Robust Control for Linear Singularly Perturbed Systems with Parametric Uncertainty".

Given this day, under the seal of the Institute at Mumbai in the Republic of India, the 11th day of July, 1996.

Chairman, Senate

Registrar

Chairman, Board of Governors

INDIRECTOR  
I.I.T. BOMBAY  
MUMBAI - 400 075



**INDIAN INSTITUTE OF TECHNOLOGY BOMBAY**

*upon recommendation of the Senate  
hereby confers the degree of*

**Doctor of Philosophy**

*on Prakash Pandurang Vaidya*

*who has fulfilled the requirements for the degree  
as prescribed under the regulations and successfully  
defended in June 1988, the thesis entitled:*

*"Methods to Improve the Performance of Nuclear ADC's."*

*Given this day, under the seal of the Institute at Bombay,  
in the Republic of India,  
the 27th day of July, 1988.*

*A. N. Nayak*  
Chairman, Senate

*Registrar*  
Registrar

*Chairman*  
Chairman, Board of Governors



# National Institute of Technology Kourkela

Upon the recommendation of the Senate,  
hereby confers the degree of

**Doctor of Philosophy**

on

*Ramesh Kulkarni*

---

who has successfully defended the thesis entitled  
*“Novel Restoration Techniques for Images  
Corrupted with High Density Impulsive Noise.”*  
and has fulfilled all other requirements  
for the degree in the Department of  
*Electronics & Communication Engineering*  
Given this day, under the seal of the Institute  
at Kourkela in the Republic of India,  
the 19th Day of January 2013.

Registrar

Director

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V.E.S. Institute Of Technology	
R E C E I V E D	INWARD SECTION
	NO. <u>59</u>
	<u>03/08/15</u>

## University of Mumbai



Th/ICD/2015-16/ 2927  
Mumbai-400 032  
23rd July, 2015.

The Principal,  
Vivekanand Education Society's,  
Institute of Technology,  
Shri Hashu Advani Memorial Complex,  
Behind Collector Colony,  
Chembur,  
Mumbai 400 074.

Sir,

I am sending below a list of students registered through your College for the M.Pharm /Ph.D. degree showing registration number assigned to each of them against the name of respective students. You are requested to bring the registration number assigned to them to the notice of the students concerned and instruct them to quote the said numbers invariably in connection with any correspondence with the University.

Sr. No.	Name of the Candidate	Subject	Registration No. & Date
1	Smt. Warke N.V.	Ph.D./Electronics Engg.	01/16-03-2015
2	Smt. Adsul J.P.	-----do-----	02/16-03-2015

Yours faithfully,

Controller of Examinations.

V.E.S. Institute Of Technology	
R E C I V E D	SECTION
	NO. 178
	29/07/16

## University of Mumbai



Th/ICD/2015-16/ 8235  
Mumbai- 400 032.  
23<sup>rd</sup> February, 2016.

**The Principal,**  
V. E. S. Institute of Technology,  
Ashu Advani Memorial Complex,  
Collector Colony,  
Chembur,  
**Mumbai-400 074.**

Sir/Madam,

I am sending below a list of students registered through your College for the Ph. D degree showing registration numbers assigned to each of them against the name of respective students. You are requested to bring the registration numbers assigned to them to the notice of the students concerned and instruct them to quote the said numbers invariably in connection with any correspondence with the University.

<u>Sr. No.</u>	<u>Name of the Candidate</u>	<u>Subject</u>	<u>Registration No. &amp; Date</u>
1	Smt. Sengupta S. S.	Ph.D./Electronic Engineering	03/07-11-2015
2	Smt. Siddavatam A. P.I.M.	-----do-----	04/09-11-2015
3	Smt. Joshi S. S.	-----do-----	05/09-11-2015
4	Smt. Sharma K. A.	-----do-----	06/09-11-2015
5	Smt. Lekshmi Ajesh	-----do-----	07/09-11-2015

Yours faithfully,

**Assistant Registrar.**



RECEIVED  
UNIVERSITY OF MUMBAI  
THESIS SECTION  
Phone - 022-2270 3248  
Phone - 022-2270 8729  
E-mail - [universitythesis011@gmail.com](mailto:universitythesis011@gmail.com)  
14/2/2018

## University of Mumbai



Thesis Section,  
UNIVERSITY of MUMBAI,  
Fort Campus, M.G. Road,  
Mumbai - 400 032.

Th/ICD/2017-18/ 4918  
26 of February, 2018.

**The Principal,**  
V. E. S. Institute of Technology,  
Ashu Advani Memorial Complex,  
Collector Colony,  
Chembur,  
**Mumbai-400 074.**

Sir/Madam,

I am sending below a list of students registered through your Institute for the Ph. D degree showing registration numbers assigned to each of them against the name of respective students. You are requested to bring the registration numbers assigned to them to the notice of the students concerned and instruct them to quote the said numbers invariably in connection with any correspondence with the University.

<u>Sr. No.</u>	<u>Name of the Candidate</u>	<u>Subject</u>	<u>Registration No. &amp; Date</u>
1	Smt. Kant N.	Ph.D./Electronics Engineering	08/04-05-2017
2	Smt. Sangeetha P. R.	-----do-----	09/17-07-2017
3	Smt. Tevari K.	-----do-----	10/17-07-2017
4	Smt. Chavan K. V.	-----do-----	11/11-12-2017

Yours faithfully,

Offg. Superintendent  
Thesis Section



Vivekanand Education Society's  
**INSTITUTE OF TECHNOLOGY**

Hashu Advani Memorial Complex, Collector's Colony, Chembur, Mumbai - 400074.  
Tel- 022 61532532; Fax - 022 61532555; Web site: [www.vesit.edu](http://www.vesit.edu) Email: [vesit@vesit.com](mailto:vesit@vesit.com)  
[ Affiliated to University Of Mumbai , Approved by AICTE and Recognised By Govt. Of Maharashtra ]

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**Ph.D ADMISSION 2019-20**

[ RECOGNISED Ph.D. RESEARCH CENTER OF MUMBAI UNIVERSITY FOR ELECTRONICS ENGINEERING ]

DATE OF INTERVIEW : 31.07.2019

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We have interviewed all the candidates who were present for the interview for the Admission of Ph.D. in Electronics Engineering Branch during A.Y. 2019-20 at V.E.S. Institute Of Technology (recognised Ph.D. Research Center of Mumbai University for Electronics Engineering) , We recommend the following person/ in order of merit:

1. Phatak Tejashree Suresh

2. \_\_\_\_\_

3. \_\_\_\_\_

4. \_\_\_\_\_

5. \_\_\_\_\_

6. \_\_\_\_\_

Subject Experts :

G. Krishnamachari

Dr. G. Krishnamachari  
(Ex-Scientist, BARC)

Dr. Nadir Charnia

Dr. Nadir Charnia  
(Prof. Electronics & Telecomm. Engg. , VESIT)

P. P. Vaidya

Dr. P.P.Vaidya  
(Professor, VESIT & Ex-Scientist, BARC)

Dr. M. Vijayalakshmi

Dr. M. Vijayalakshmi  
(Vice Principal, VESIT)

Dr. J. M. Desai

Dr. J. M. Desai  
(Principal , VESIT)